Training physical models of deep spiking neural networks

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Neuromorphic computing

νεŭρον + μορφή

Neuromorphic systems replicate (aspects of) the nervous system to perform computation.



* Surace and Pfister, 2015 ** Hodgkin and Huxley, 1952

BrainScaleS-2



- mixed-signal neuromorphic system, 65 nm
- physical emulation of neuronal and synaptic dynamics

BrainScaleS-2



- mixed-signal neuromorphic system, 65 nm
- physical emulation of neuronal and synaptic dynamics
- 512 (multicompartment-capable) neurons
- 512 × 256 synapses

• LIF + AdEx dynamics

• flexibly parameterizable

- multicompartment-capable
- 1000× accelerated



Billaudelle, Weis, et al., 2022

- in-memory-compute arrays
- local weight storage (6 bit + sign)
- programmable plasticity

Friedmann, Schemmel, et al., 2017

Training multi-layer spiking neural networks

Spatial credit assignment



- → calculate gradients of a loss function L w.r.t. network parameters
- → propagate errors through network layers by computing gradients

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Temporal credit assignment



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- capture temporal propagation of information
- → construct multidimensional compute graph
- → backpropagate through time (BPTT, Werbos, 1990)

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Gradients of spikes?



- gradients can be estimated only in presence of spike
- spikes introduce discontinuities in parameter space
- → adopt surrogate gradients (Neftci, Mostafa, et al., 2019)

Training event-based analog systems





Training event-based analog systems

Cramer, Billaudelle, et al., 2022



<u>카</u> = ?

Training event-based analog systems



Highly streamlined programming interface

```
network = strobe.nn.Network([
    strobe.nn.Linear(n_input, n_hidden),
    strobe.nn.LIFLayer(n_hidden, n_output),
    strobe.nn.LILayer(n_output)
  ])
network.connect(...)
y = network(x)
loss = my_loss(y, y_prime)
loss.backward()
```

built on top of



Classification of handwritten digits

Cramer, Billaudelle, et al., 2022



60 80 100



Cramer, Billaudelle, et al., 2022



< 8 µs











Cramer, Billaudelle, et al., 2022



< 8 µs

latency

84 k/s

200 mW

power

2.4 μJ energy

Ultra-efficient messaging within the network

Cramer, Billaudelle, et al., 2022

- during training: optimize for reduced network activity
- → extreme temporal sparsity

only 0.08 spikes per neuron!



Robustness to variability in analog circuits



- analog circuits suffer from production-induced parameter variability
- train on deliberately detuned neuron dynamics
- → training scheme inherently resilient to these effects

Recurrent spiking neural networks for speach recognition

Cramer, Billaudelle, et al., 2022

»null«

76.2 % (96.6 %) on BrainScaleS-2

Summary

- high-level user interface, integrated into PyTorch
- robust, self-correcting training

- efficiency through analog computation and event-based communication
- low classification latencies
- high throughput

Thanks!

Sebastian Billaudelle

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