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Implementation and Validation of a Balanced Excitatory-Inhibitory Network in Loihi

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We implement a balanced excitatory/inhibitory network (EI) in Intel's neuromorphic hardware Loihi. A version of [1] has been used by researchers as a benchmark and validation for simulators in various software and hardware environments [2]. The implementation here has the same LIF neurons, but exponential decay synapses, and reduced size, which accommodate current software and hardware limitations of Loihi [3]. We implement the same network in NEST for validation.

The Loihi implementation was designed as in [4], by scaling and shifting the LIF models and their parameters in order to fit within Loihi's integer state arithmetic and limited-precision storage for parameters. For the network implementations here, we observe visually very similar firing patterns in both NEST and neuromorphic implementations. Network rate and correlation comparisons as in [5] yield similar numeric results, although not at the level of comparisons between standard simulations on varied architectures.

The NEST implementation of the EI network runs approximately in real time, ~20s/s biological time, scaling weakly with network size [2]. The Loihi simulation performs much faster, as expected for a specialized hardware simulator. It exhibits a similar weak scaling, running about 500x faster than a standard CPU implementation, and 25-50 times faster than biological real time. Power consumption was also much lower, at approximately constant 2W/chip, with up to 6 chips used for these simulations.

In conclusion, Loihi shows promise as an accelerator for biological neural network simulations. However more studies are needed to fully qualify its benefits and trade-offs of this platform.

Acknowledgements

References

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